Historical data list setting steps

-、Set the record buffer

Open the application—set the working parameters(F7) **Solution**, select the record buffer, this case is set as follows:

m. set Network	set Alarm/Other Fig	gure/Language
General HMI PRM: N Usb Disk Dat Pe	АТБО7ОН (800*480) - rmis.: Super - Г	HMI Match Select Table Link2 Use
Linkl Set u	p	
Port: COM1	 Device ty CoolMay F 	PLC (2N) 🗾
Rate: 9600	▼ Timeout: 200 ms	Equipment 1
CheckBit: Even	▼ Dat Bits: 7 b ▼	Stop bit: 1 b 💌
Attempts 1 <mark>8</mark>	Fast reading 0	Data leng 0
Link2 Set u	p	
Port: COM2	Device ty Mitsubish	ni FX2N 🔽
Rate: 9600	Timeout: 200 ms	Equipment 0
CheckBit: Even	Dat Bits: 7 b	Stop bit: 1 b 💌
Attempts 18	Fast reading 0	Data leng 0

Network set	Ala	rm/Other	Fi	gure/	Langua	age l	Recor	d but	ffer		• •
Buffer# Data	sourc	eD Trigger	flag	#ach	leng	tTotal	sum Ai	ito s	top Rec	ordi	ng
🔽 Record 10	\$	0	\$	6	•	10000	\$	Г	10	\$	Sec
🗆 Record 🖸	\$		•	1	\$	1	\$		10	\$	Sec
🗆 Record 🛛	\$		\$	1	-	1	\$	Г	10	\$	Sec
🗆 Record 🖸	\$		\$	1	•	1	\$		10	\$	Sec
🗆 Record 🛛	\$		\$	1	\$	1	\$	Г	10	\$	Sec
🗆 Record 🛛	•		•	1	\$	1	\$		10	\$	Sec
🗆 Record 🛛	\$		•	1	\$	1	\$	Г	10	\$	Sec
🗆 Record 🛛	•		\$	1	\$	1	\$		10	\$	Sec
Record O	\$		\$	1	\$	1	\$	Г	10	\$	Sec
🗆 Record 🛛	\$	F 0	\$	1	\$	1	\$		10	\$	Sec
Record O	\$		\$	1	\$	1	\$	Г	10	\$	Sec
🗆 Record 🛛	\$		\$	1	\$	1	\$		10	¢	Sec

Buffer# 1

1. Buffer# Record : A recording area can record up to 10 16-bit registers or 5 32-bit registers for a total of 12 recording areas. However, due to limited memory, it is not recommended to record

too much register data; [this case only checks the record 1]

Data sourc

2. Data sources D^{l} : Indicates the first address of the recorded register; this case is set as D10

Trigger flag

General HMI PRM: Usb Disk Dat F	MT6070H (800*480 Permis. : Super) - HMI M r - Link2	atch Select T 2 Use	able
Linkl Set				
Port: COM1	Device ty	CoolMay PLC(2N)		-
Rate: 9600	▼ Timeout:	200 ms	Equipment 1	
CheckBit: Even	▼ Dat Bits:	7 b 💌	Stop bit: 1 b	. •
Attempts 18	Fast read	ing 5 🔹	Data leng-20	\$
Link2 Set	up			
Port: COM2	Device ty	Mitsubishi FX2N	1	_
Rate: 9600	▼ Timeout:	200 ms	Equipment 0	
CheckBit: Even	T Dat Bits:	7 в 💌	Stop bit: 1 b	-
Attempts 18	Fast read	ing 0	Data leng 0	\$

The address of the trigger flag corresponds to D5.0; If the M0 is used as the trigger flag in the PLC, the program in the PLC is written as follow:

	M8000				
0		MOV	K1MO	D5	1
Ĭ		Linov	ITTINO	DO	

It means the status of M0 is given to D5.0.

And note: the register to be recorded and saved also needs to be placed in the fast read area. The fast reading area is set to D5, and the data length is set to 20, indicates that the fast read area contains 20 registers of D5-D24, which contains four registers D10, D11, D12, and D14 of the current program to be recorded.

#ach leng

4、 Each length 6 主 : Indicates that several registers can be recorded, and up to 10 16-bit

registers or 5 32-bit registers can be recorded; **[** this case is set to 6, record two 16-bit data (D10, D11) in the history curve and two 32-bit data in the history data list (D12, D14) **]**

Total	sum A
	D Cont as

5、Total sum 10000 : Indicates the total number of records of data, up to 10,000; [this case is set to 10000]

6. Auto stop : It means that after the total number of records is recorded, it can be automatically stopped or automatically overwritten. The check is automatic stop, and the uncheck is automatic overwritten. [this case is set to automatic overwritten]

pRecording in

7、Recording interval 10 Sec. : Indicates that the save can be triggered at intervals, the record interval is in seconds S, and the record buffer can only save data by trigger flag or record interval. [This case has been triggered with a trigger flag, so the recording interval does not need to be set.]

\Box Set the history data list

o stop:

1. Add historical data list components



2. The basic properties of the record buffer # select 1, the table refresh flag # select 0, the current record number \$ W1000, you can change it by yourself (how many pens does touch screen internal register display), the maximum number of records changed to 10000, if No password , protection is selected no;

Historical data list a	Basic attri Colu 1 Colu 2	Colu 3 Colu 4
Left: 118 Top: 92 Property Wide: 302 High: 180 Backg:	Record buffer 1 • Table refresh 0 • Current recor 1000 \$ 32bit Maximum recor 10000 \$ Display mode: Antito •	Display fq8X16 💌 Table coluUse 💌 Table lineNo us 💌
		Confirm(Y) Cancel(N)

2. The first column is the date, which can change the font color and column width more basic information;

Positi Locked Left: 118	Basic attri Colu 1 Colu [Display t:Languag 日期	2 Colu 3 Colu 4 🛛 📥
Top: 92 🔹 Property Wide: 302 🔹 High: 180 🔹 Backg:	Languag Date Time format C HH: MM:SS C YYYY-MM-DD C YYYY-MM-DD HH: MM:SS C YYYY-MM-DD-HH: MM:SS F YYYY-MM-DD/HH: MM:SS C YYYY-MM-DD HH: MM:SS	Font colo: Col width 🔂 🌩 Align mod Left 💌 🔽 Show this

3 . In the second column, the data position is 0, that is D10, and the data type is 16 bits;

Positi Locked Left: 47 🔹 Top: 52 🔹	Colu 1 Colu 2 Colu 3 C [Display t:Languag No. Languag NO.	olu 4 Colu 5 Colu 6 💶 🕨
Property Wide: 520	Data positi 0 🗘 Data type: 16 Bit 💌	Font colo:
High: 301 🛊 Backg:	Data format Usigned 💌 Integer: 3 🜩 Decimal dig:0 🜩	Align mod Left ▼ Show this

. In the third column, the data position is 1, that is D11, the data type is 16 bits;

Positi Colu 1 Colu Locked [Display t: La Ton: 52	2 Colu 3 Colu nguag Number nguag Number	14 Colu 5 Colu 6 🖪	•
Property Wide: 520	Ĵ Bit ▼	Font colo:	
High: 301 🔹 Data format U Backg: 💽 🔽 Integer: 9 Grid: 💽 Decimal dig 0	nsigned 💌 🜩	Align mod Left ▼ ▼ Show this	

. In the fourth column, the data position is 2, that is D12, and the data type is 16 bits;

Positi Locked Left: 47 🔹 Top: 52 🔹	Colu 1 Colu 2 Colu 3 C [Display t:Languag Good Languag Good	olu 4 Colu 5 Colu 6 🔺
Property Wide: 520	Data positi 2 🗘 Data type: 16 Bit 💌	Font colo:
High: 301 🔹 Backg:	Data format Unsigned - Integer: 9 - Decimal dig:0 -	Align mod Left ▼ Show this

6 . In the fifth column, the data position is 3, that is D13, and the data type is 16 bits;

Positi Locked eft: 47	Colu 1 Colu 2 Colu 3 ([Display t: Languag Inferio	Colu 4 Colu 5 Colu 6 💶
op: ⁵² 💌 Property Fide: 520 📤	Data positi 3 🔶 Data type: Float 💌	Font colo:
ligh: 301 🔹	Data format Unsigned Integer: 5	Align mod Left 💽
rid:	Decimal dig v	

Note: One 16-bit register occupies one data location, and one 32-bit register occupies two data locations.

7、 Add the record buffer #01 cleared function key:



Position Locked	Basic Transpar Lang	uag Clear #1	-
eft: 501 🚖	Touch efi Lang	uag FunKey	-
op: 157 🚖	Set format	Senior Function	
roperty	Borde luxury 💌	Record buffer #01 clea 💌	
ide: 55 🌢	Font: 8X16 -	Record buffer #01 clean	
ligh: 25 🚖	Align Midd 💌	Record buffer #03 clear Record buffer #04 clear	
ackg:	Effec Level:Gent	Record buffer #05 clean	
rosp:	C Basic fun G Senior fu	Record buffer #06 clean Record buffer #07 clean Record buffer #08 clean -	

Add the register that needs to be stored D10-D13:



Reg attribute		X
Position Locked Backg tra Left: 525 \$ Top: 122 \$ Property Wide: 42 \$ High: 22 \$ Backg: \$ Prosp: \$	Reg Chann conn: Link 1 V Set po Elem type: D V? RegisterD: 10 P Data type: 16 Bit C Indire Set format Bit num4 Borde: 3D V Decimal Password Grade Ordinar	ermi Signed Zero 1 t upp 65535
Reg attribute		Confirm(Y) Cancel(N)
Position Locked Backg tra Left: 532 Top: 153	Reg Chann conn: Link 1 V Set po Elem type: D V? © Direc RegisterD: 11 © Direc Data type: 16 Bit V © Indire	ermi Signed Zero 1, t upp, ectMaxD t low, ectMinD
Property Wide: 42 🔹 High: 22 🔹 Backg: 💽 Prosp:	Set format Bit num4 Borde:3D R Decimal 0 Font: 8X16 0 NoticeSet Align Midd 0 □ Password Grade Ordinar 7	Ctrl reg RegisterM: O
	6	Confirm(Y) Cancel(N)

Reg attribute		
Position Locked Backg tra Left: 535 \$ Top: 190 \$	Reg Chann conn: Link 1 • Elem type: D • ? RegisterD: 12 • Data type: 16 Bit •	 ✓ Set permi Signed Zero 1. ✓ Direct upp. 65535 ✓ IndirectMaxD ✓ Direct low. 0 ✓ IndirectMinD
Property Wide: 42 🔹 High: 22 🔹 Backg: 💽 Prosp:	Set format Bit num 4 🔹 Borde: 3D Decimal 0 🜩 Font: 8X16 NoticeSet Align Midd 🗆 Password Grade Ordina	Ctrl reg RegisterM: O Ctrl func No use Ctrl func No use Cond exec Show **** Password input/ Confirm(Y) Cancel(N)
Reg attribute		×
Reg attribute Position Locked Backg tra Left: 537 Top: 227	Reg Chann conn: Link 1 Elem type: D RegisterD: 13 Data type: 16 Bit	 ✓ Set permi ⊂ Signed ⊂ Zero 1. ○ Direct upp. 65535 ○ IndirectMaxE ○ Direct 1ow. 0 ○ IndirectMinE
Reg attribute Position Locked Backg tra Left: 537 Top: 227 Property Wide: 42 High: 22 Backg: Prosp: Pro	Reg Chann conn: Link 1 Elem type: D RegisterD: 13 Data type: 16 Bit Set format Bit num 4 Borde: 3D Decimal Font: 8X16 NoticeSet Align Midd Password Grade Ordina	 ✓ Set permi Signed ☐ Zero 1 ⑦ Direct upp: 65535 ⑦ IndirectMaxD ⑦ Direct low: 0 ⑦ IndirectMinD ⑦ Ctrl reg ⑦ Ctrl reg ⑦ Ctrl func No use ♥ Cond exec Show **** ♥ ■ Password input/

9. Set the storage mode: generally set to a fixed time (seconds) storage; as follows: set to store1 time in 1 minutes

Alarm/Other	Fig	Figure/Language			Record buffer			Recipe setting			
Buffer# Sourc	es D	Trigge	r flag	#ach	leng	tTotal s	sum Au	ito s	to <mark>o</mark> Rec	ording	
Record 10	•		•	6	•	10000	\$	Г	60	🗘 Sec	
🗆 Record 🖸	\$		•	1	•	1	\$	Г	10	🔹 Sec	
🗆 Record 🖸	-		\$	1	•	1	\$	Г	10	🔹 Sec	
🗆 Record 🖸	\$		\$	1	\$	1	\$	Г	10	🔹 Sec	
🗆 Record 🖸	\$		\$	1	\$	1	\$	Г	10	🔹 Sec	
🗆 Record 🛛	\$		•	1	•	1	\$	Г	10	🔹 Sec	
🗆 Record 🛛	\$		\$	1	\$	1	\$	Г	10	🔹 Sec	
🗆 Record 🛛	\$		\$	1	•	1	\$	Г	10	🔹 Sec	
🗆 Record 🛛	•		\$	1	•	1	\$	Г	10	🔹 Sec	
🗆 Record 🛛	\$		\$	1	\$	1	\$	Г	10	🔹 Sec	
🗆 Record 🛛	\$		\$	1	\$	1	\$	Г	10	🗘 Sec	
🗆 Record 🖸	\$		\$	1	\$	1	\$	Г	10	🔹 Sec	

10. Compiled program is as shown in the figure, after compilation,

download to the HMI:

Арр	olication(<u>A</u>) Debug(<u>L</u>) In	divid		
	Compile(<u>C</u>)	F5	Application(<u>A</u>) Debug(<u>L</u>) Indi	vid
2	Download(<u>D</u>)	F6	Compile(C)	F5
	Set working PRMS(<u>W</u>)	F7	Download(D)	FØ
1	Batch modify(P)	F8	Set working PRMS(W)	F7
GIU OF OF	Ppdate V5.71 P HMI IP: 222.222. P HMI LR: Local • P TIM Up: No •	222.222 Remote rou 🗖 Auto dow	ter start port: 50000 nload	
Ge	et network card inform	ation	2018-07-18 11:49:52	

